

GA-F2A85XN-WIFI

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03	BLOCK DIAGRAM
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05	APU CONTROL
06	APU UMI, GFX, GPP
07	APU POWER & GND
08	DDRIII CHANNEL A
09	HUDSON UMI, PCIE, PCI, CPU, LPC
10	HUDSON ACPI, GPIO, USB, AUDIO
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13	PCIE x16
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APU_VDDIO_SUS=DDR15V
APU_VTT_SUS=DDRVTT
APU_VDDP_RUN=APU_VDDR_RUN=APU_VDDP

+1.1V_RUN=FCH_VDD_11_RUN=VCC_SB

+3.3V_RUN=VCC3
+3.3V_ALW=3VDUAL

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Title

COVER SHEET

Size

Custom

Document Number

GA-F2A85XN-WIFI

Rev

1.01

Date:

Friday, May 31, 2013

Sheet

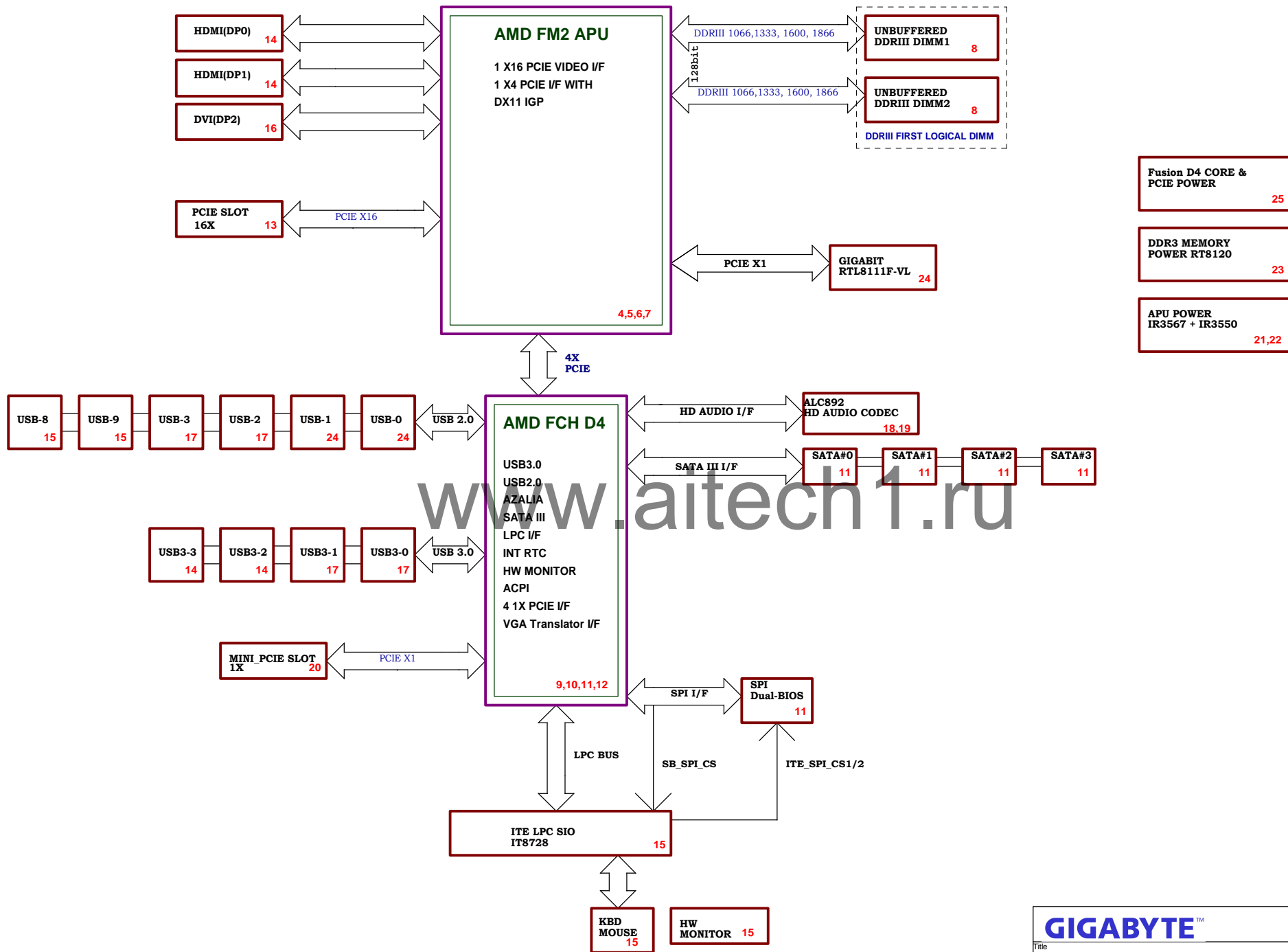
1

of

26

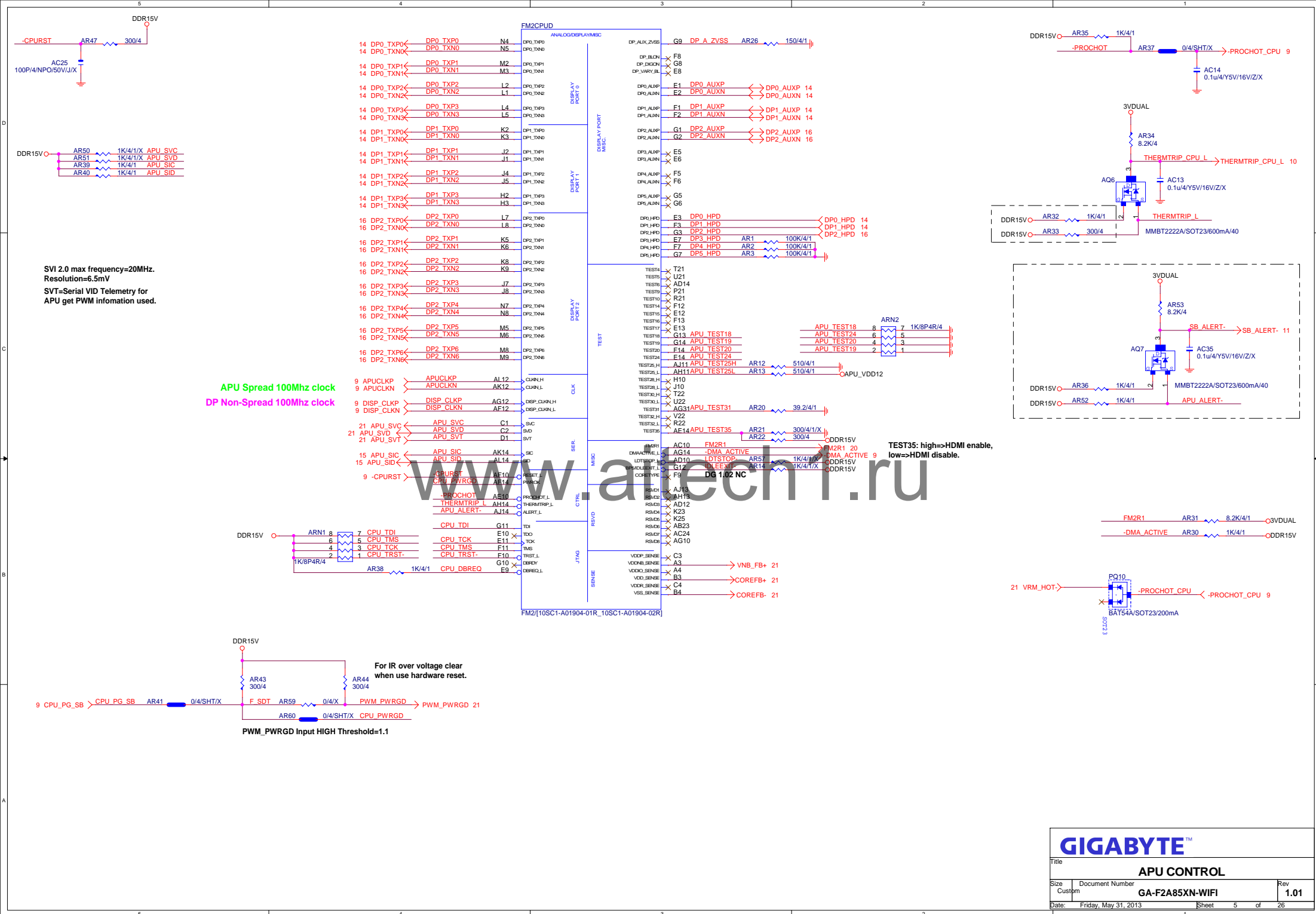
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Version: 1.01
P-Code: U12070-0

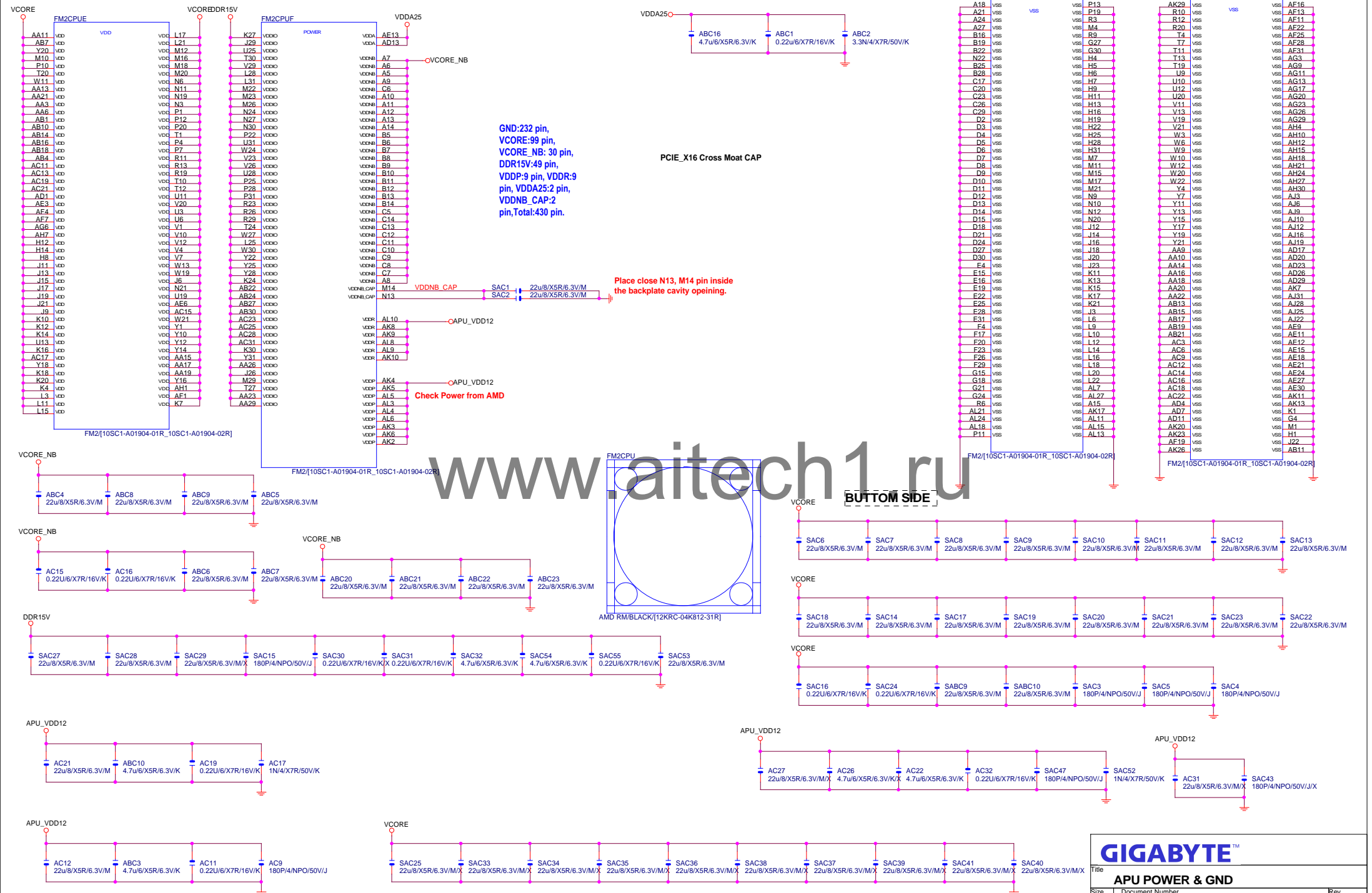


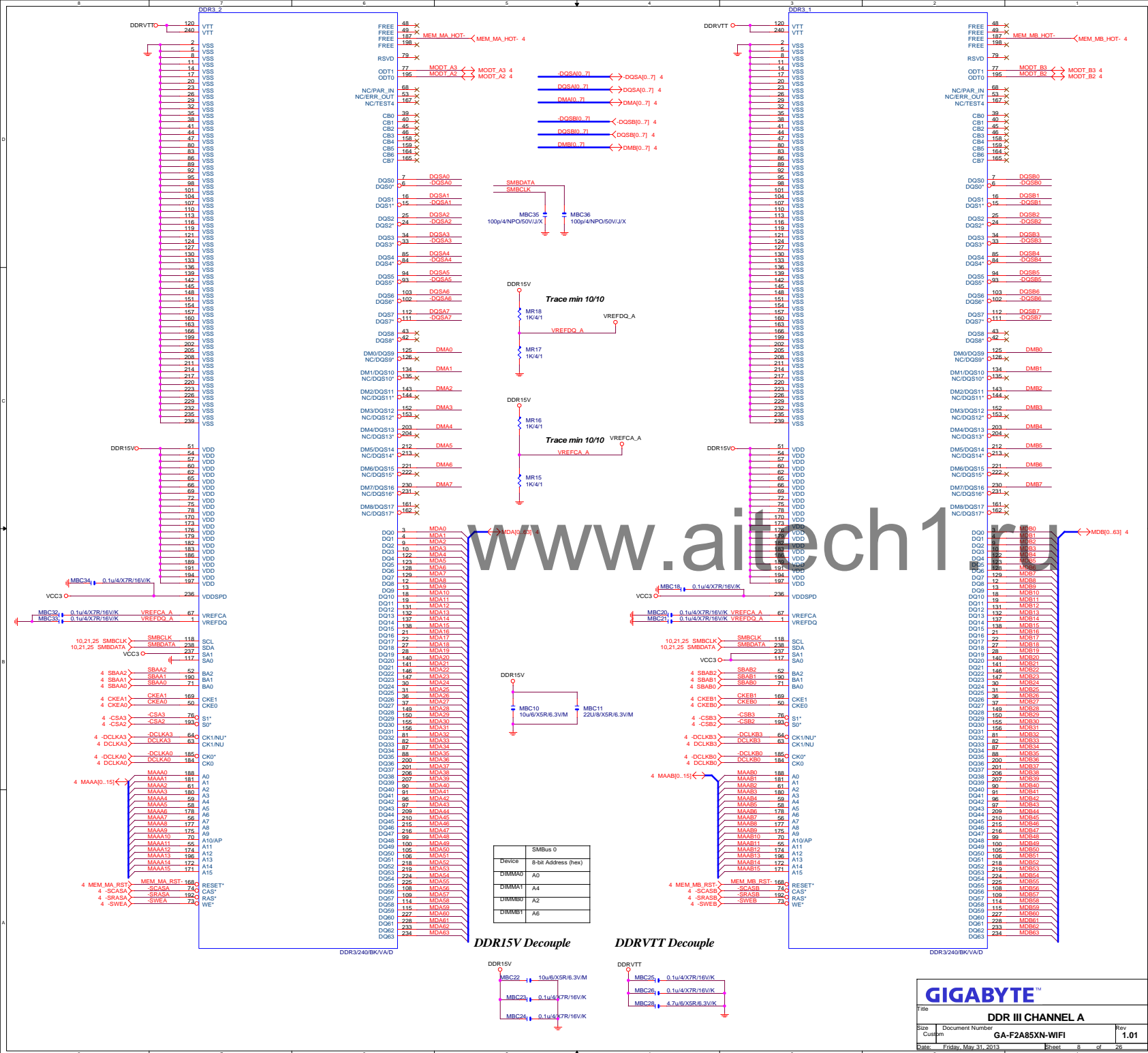
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Title BLOCK DIAGRAM			
Size Custom	Document Number GA-F2A85XN-WIFI		Rev 1.01
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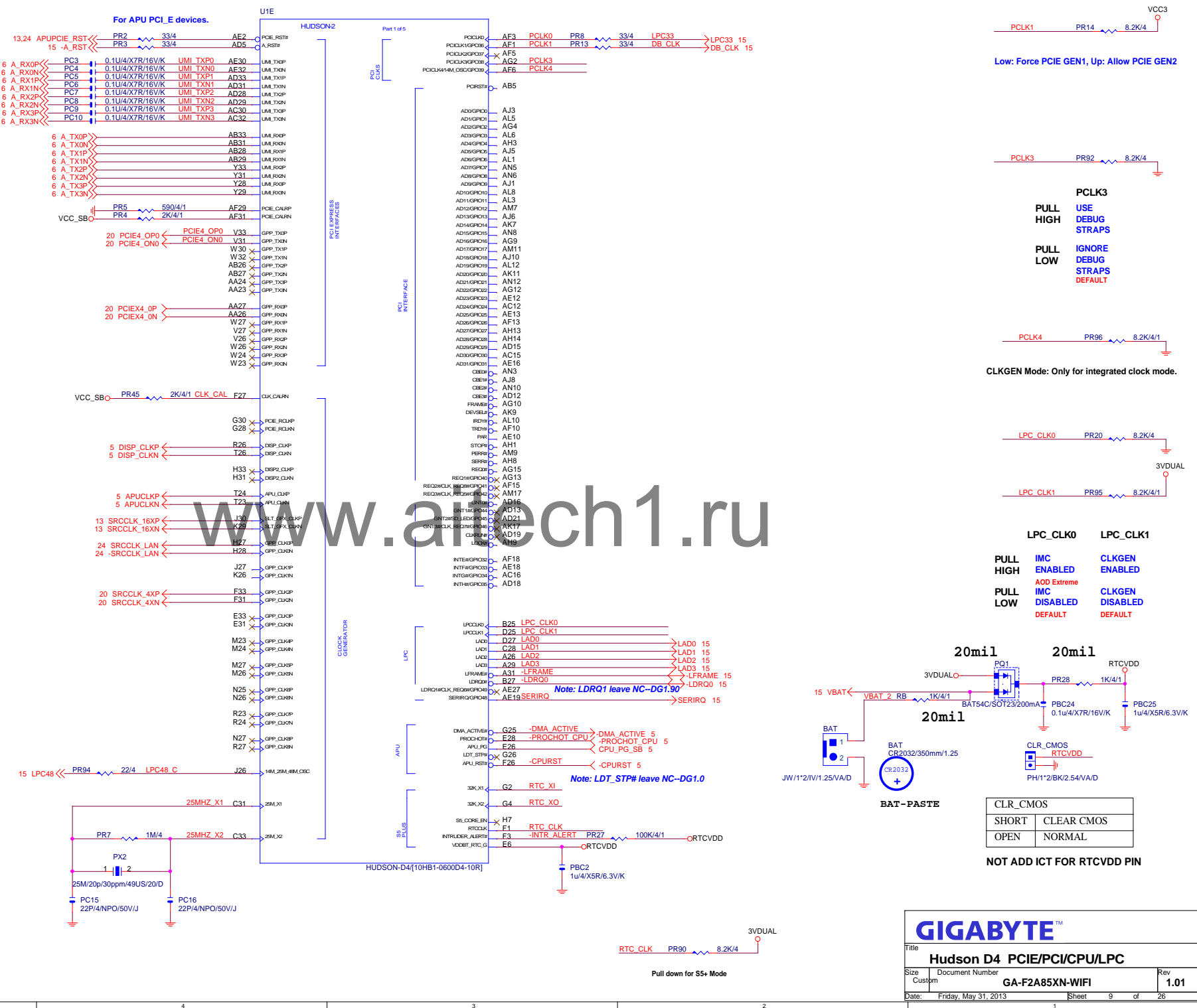
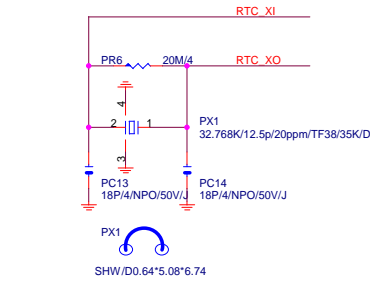
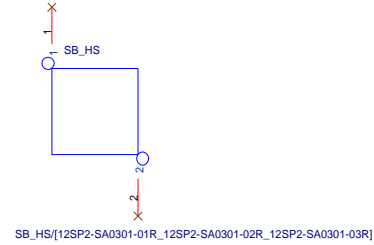
DDR15V=1.25V/1.35V/1.5V(DDR3)

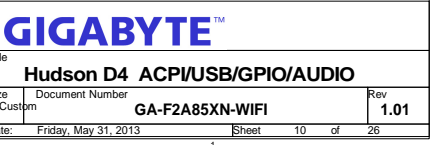


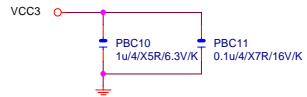


PLACE THESE PCIE AC COUPLING CAPS CLOSE TO SB850

S.B HEATSINK

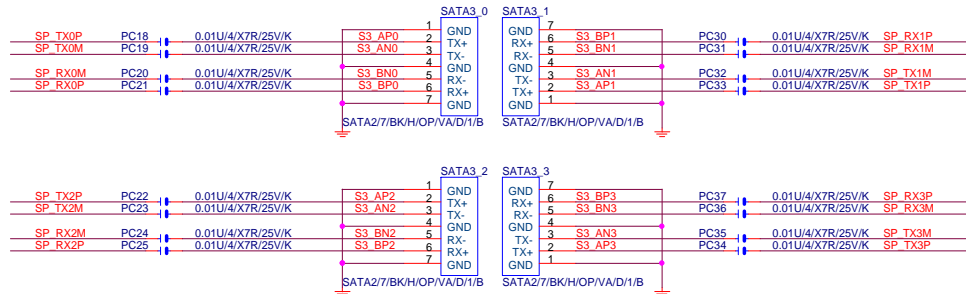






SATA 6~7 for Hudson D4.

PLACE SATA_CAL RES VERY CLOSE TO BALL OF UI

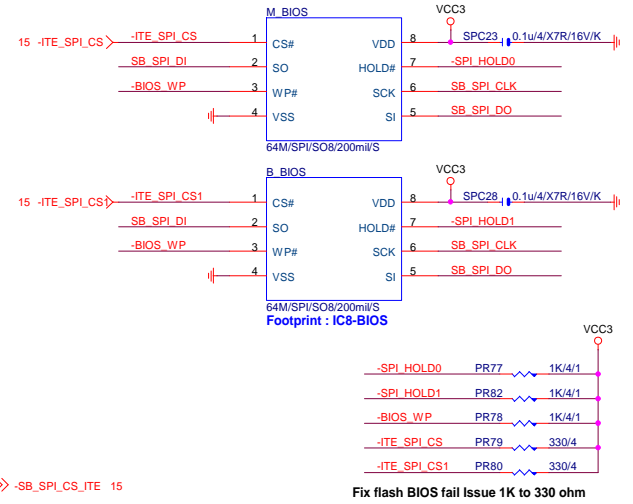


Part 2 of 5

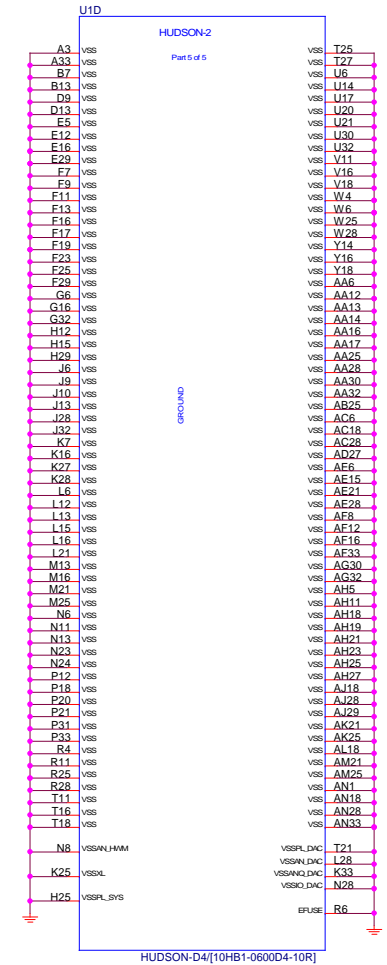
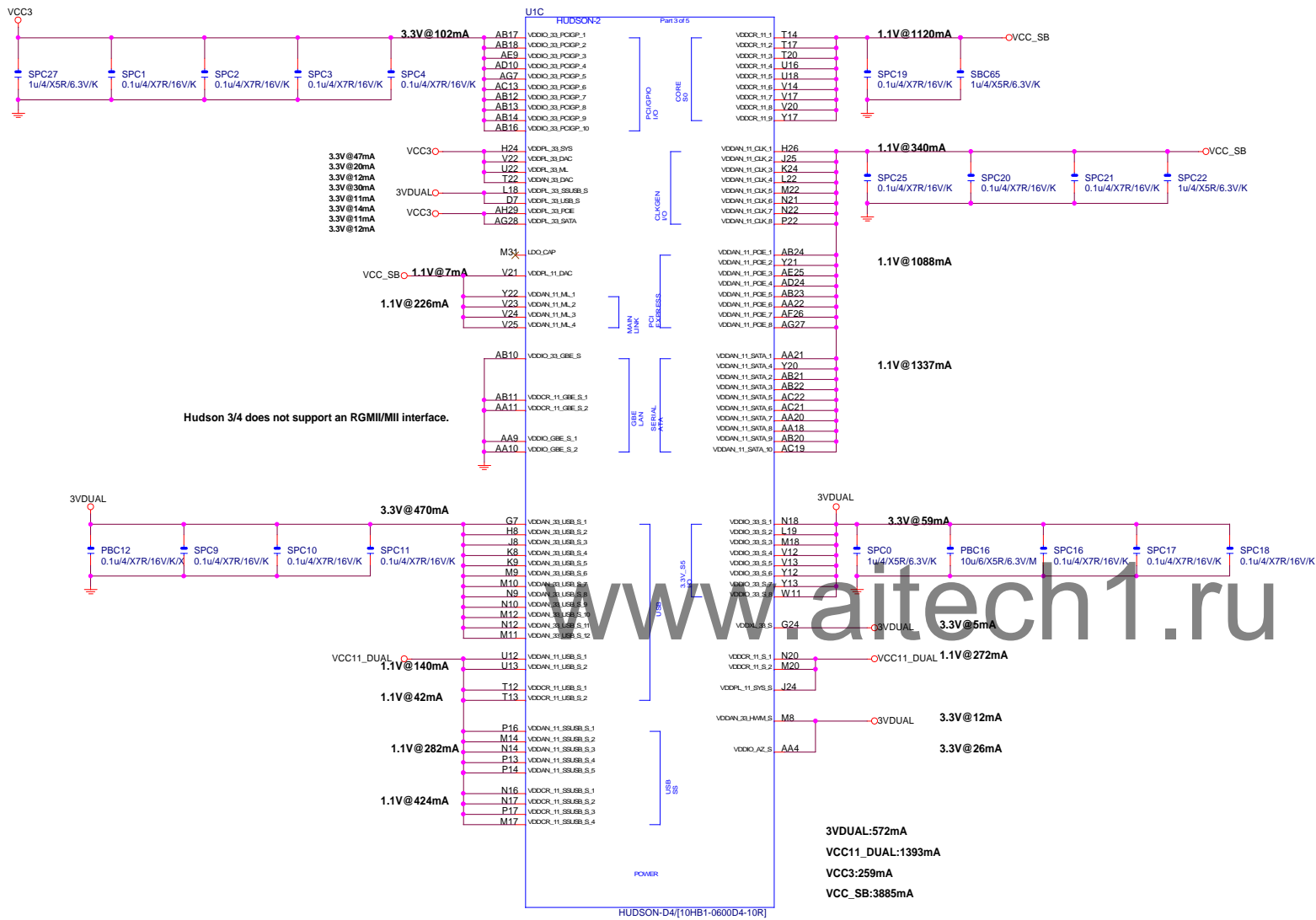
RGMI NOT SUPPORT. DG:1.20

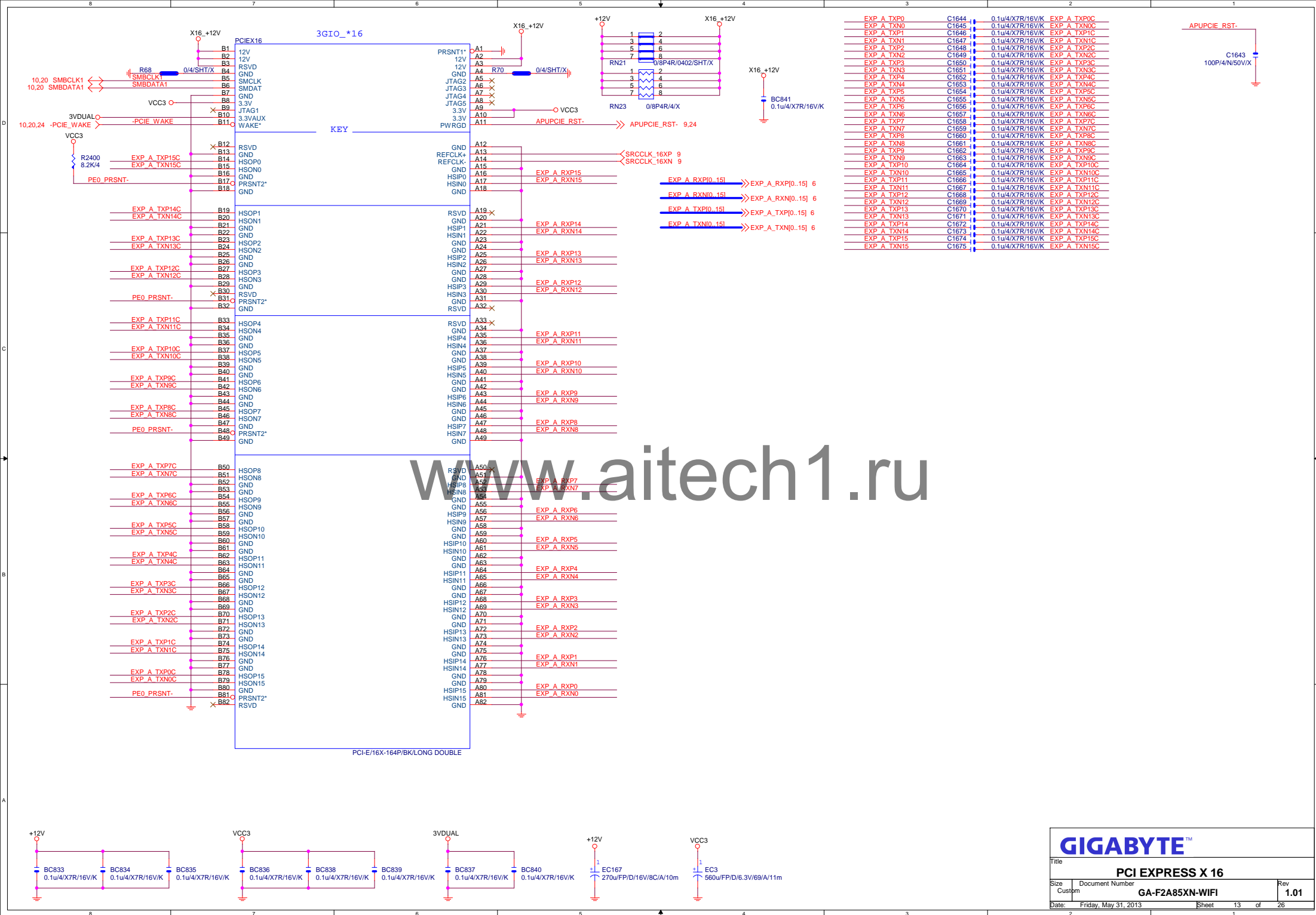


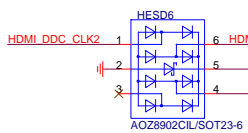
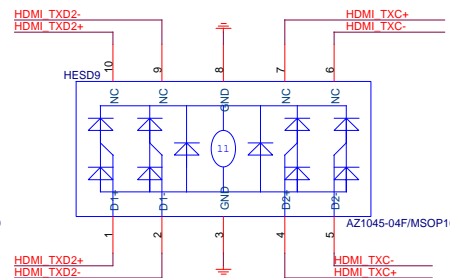
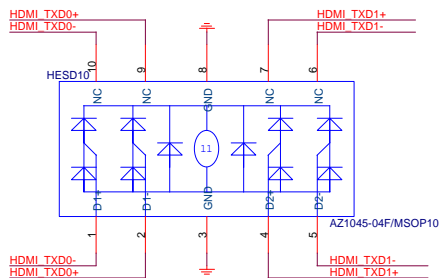
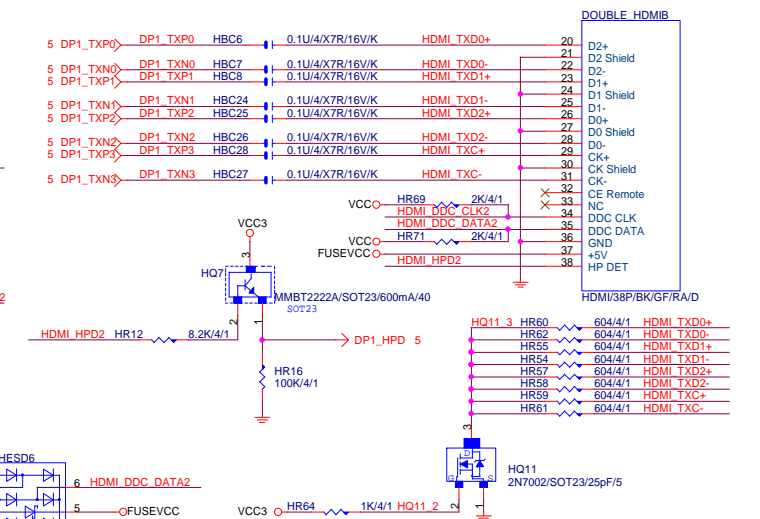
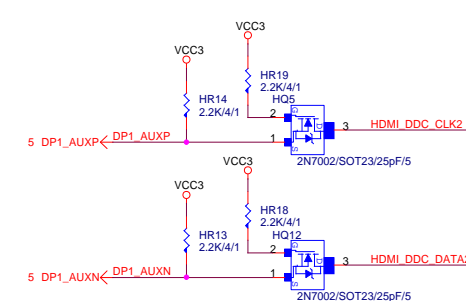
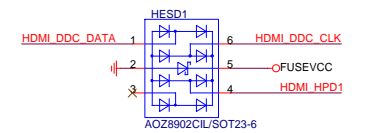
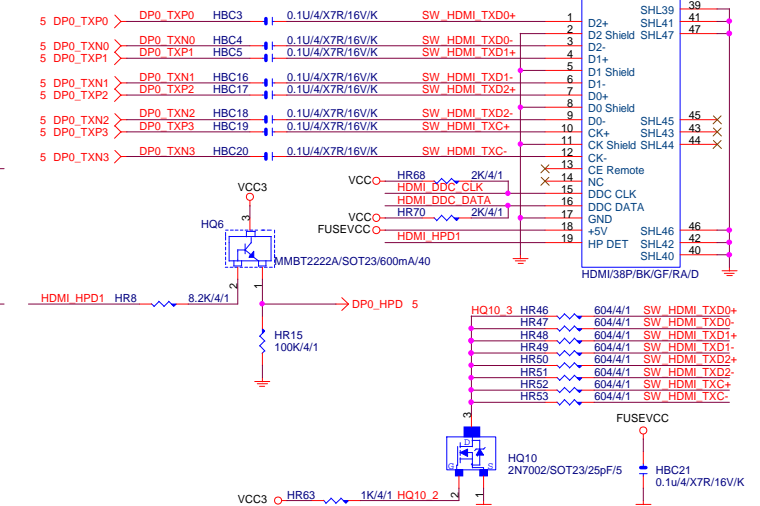
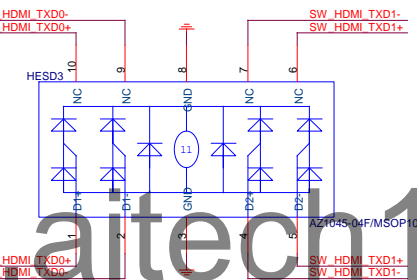
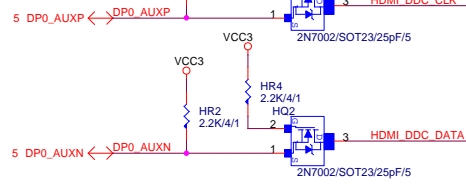
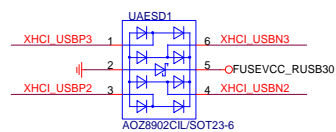
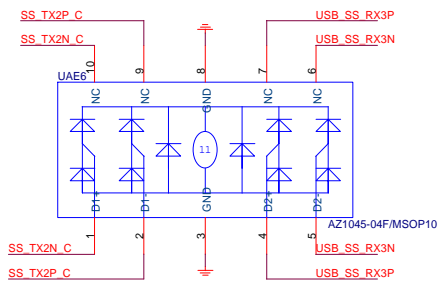
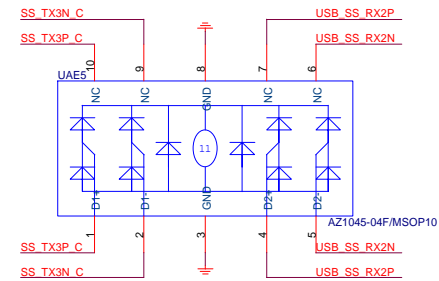
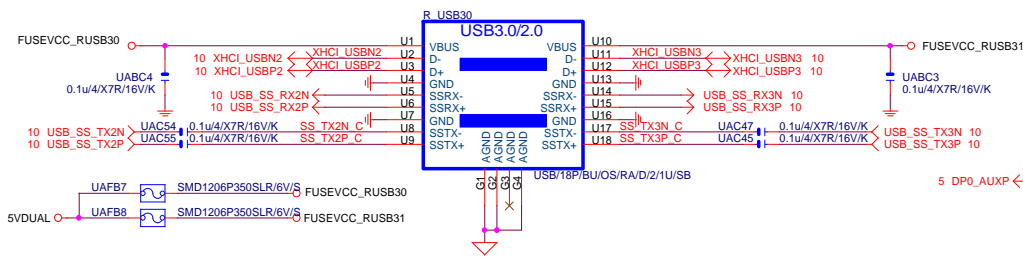
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Fix flash BIOS fail issue 1K to 330 ohm





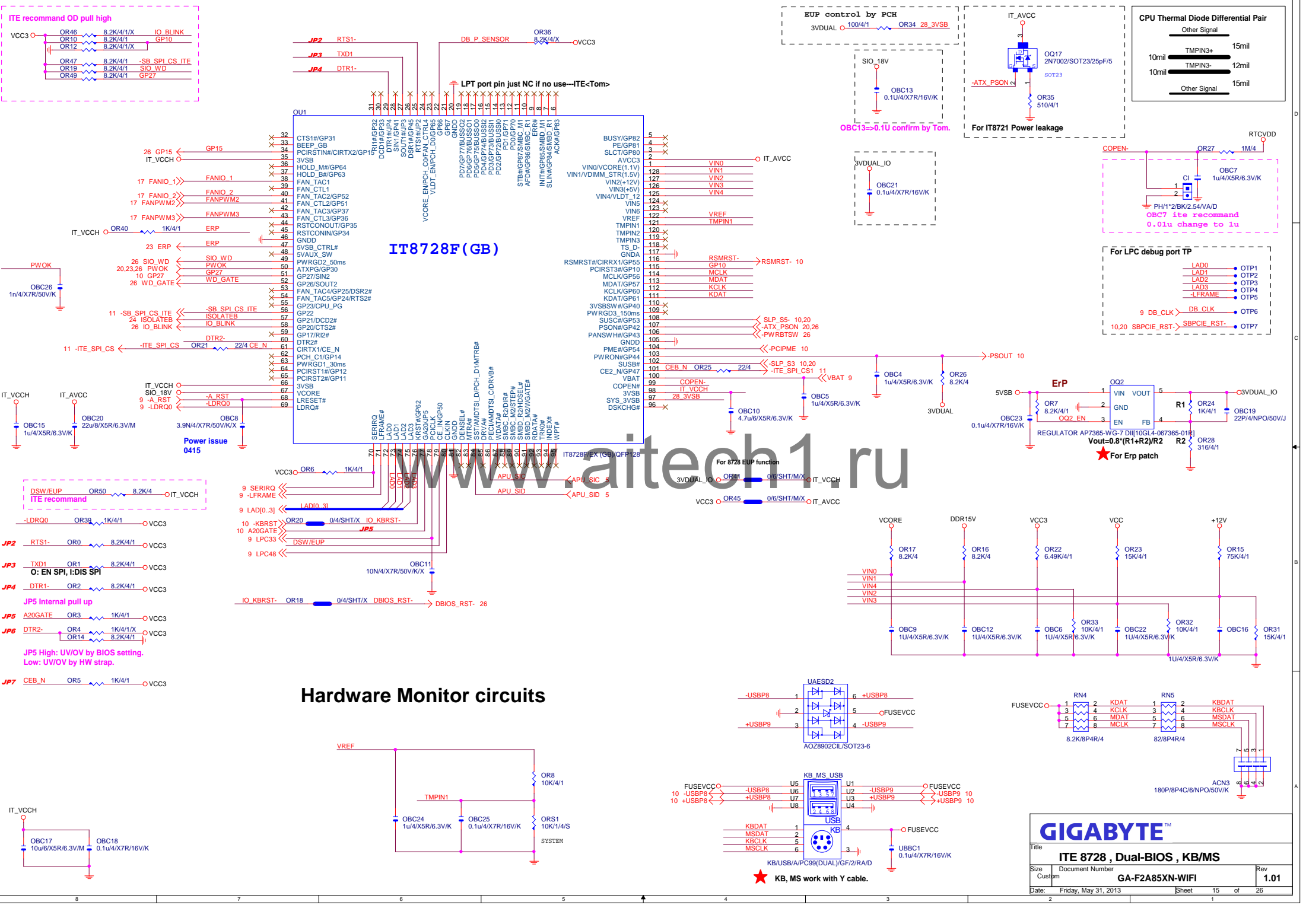


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Title **HDMI_R_USB30**

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Hardware Monitor circuits

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Title: **ITE 8728 , Dual-BIOS , KB/MS**

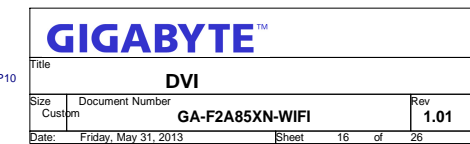
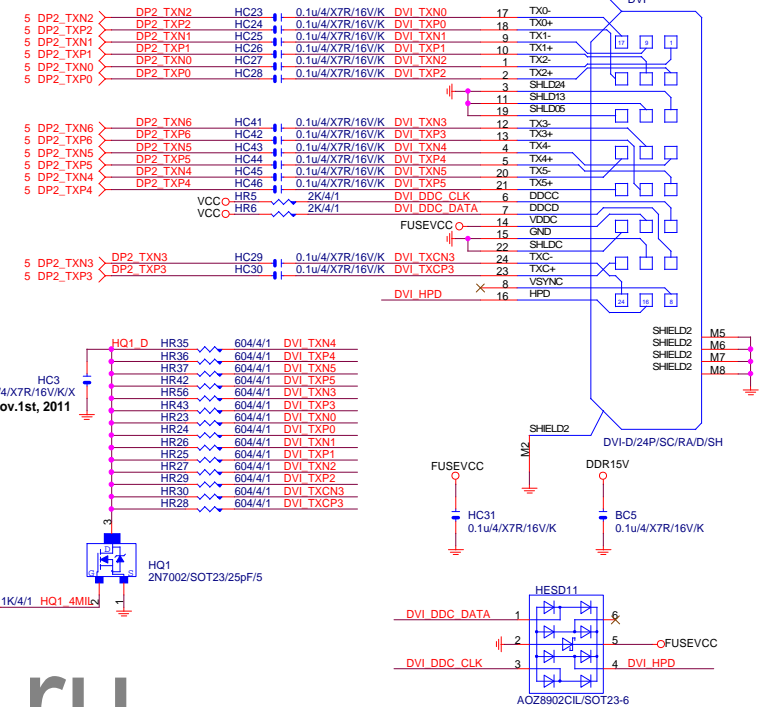
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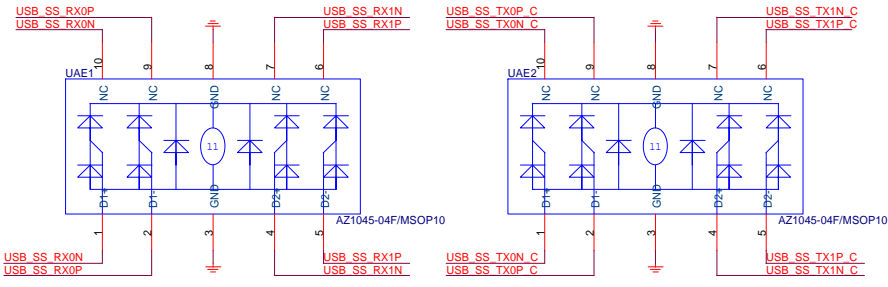
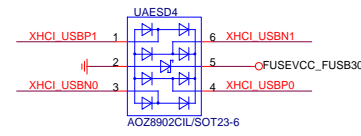
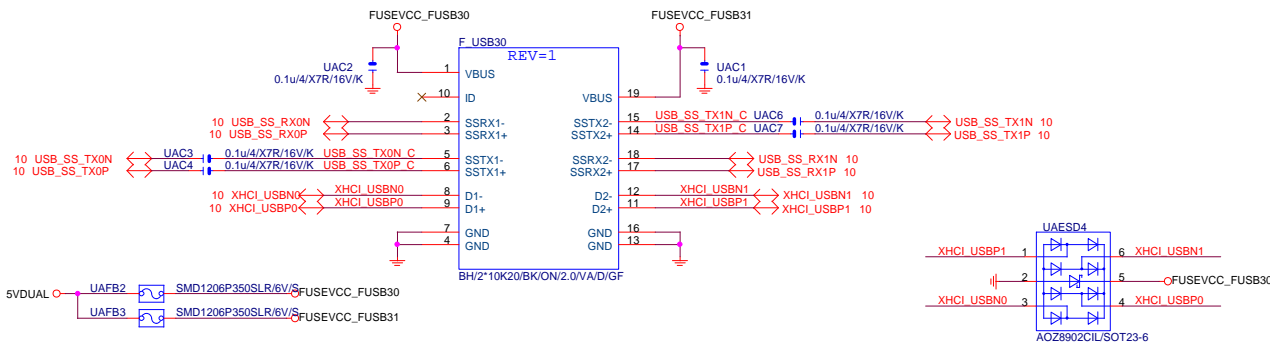
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Date: Friday, May 31, 2013

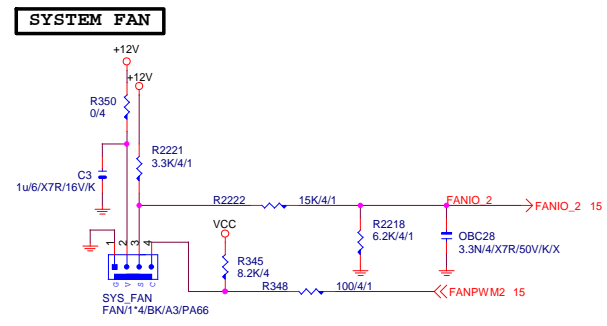
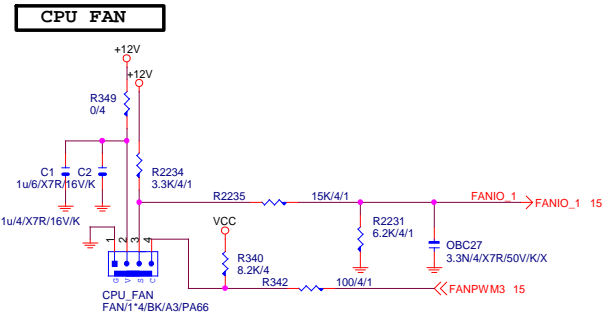
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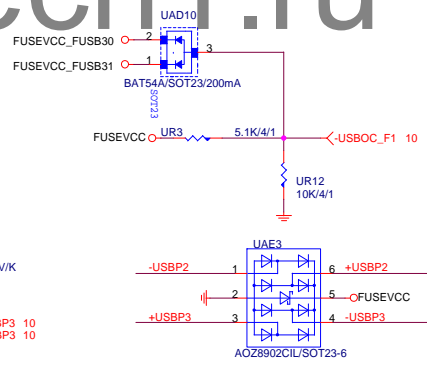
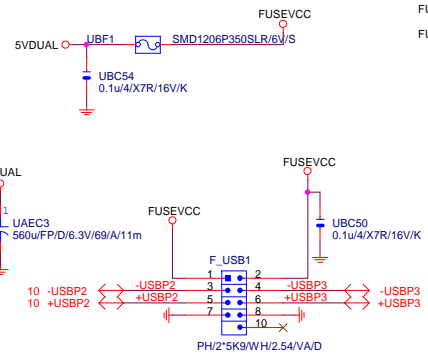




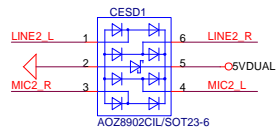
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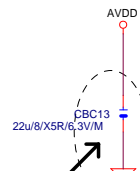
FRONT SIDE USB1



CR5/CR8/CR11/CR4/
CR17/CR22/CR45/CR33/
CR47/CR40/CR26/CR37/
CR13/CR11/CR57/CR53

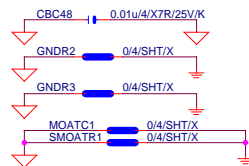


CODEC POWER/EMI PAD

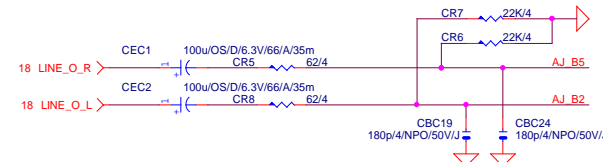


上ALC892時,此顆電容要保留

ADD CD2 For ESD PROTECT DIODE

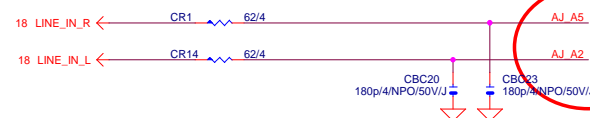


LINE-OUT



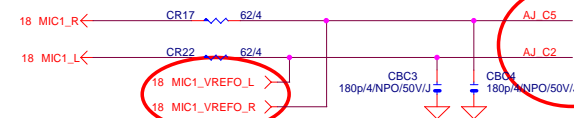
Only reserved For ALC888

LINE-IN

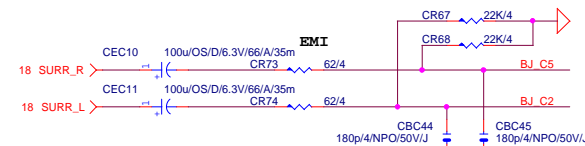
Verify MIC function
in LINE-in

For 889A/888

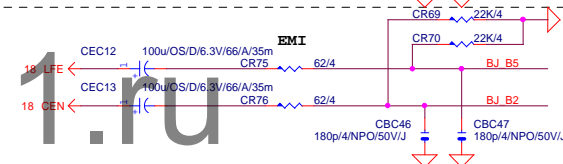
MIC-IN



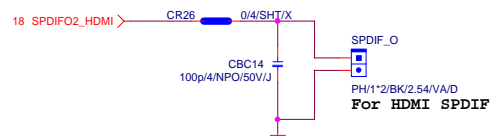
SURROUND



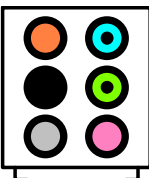
CEN/LFE



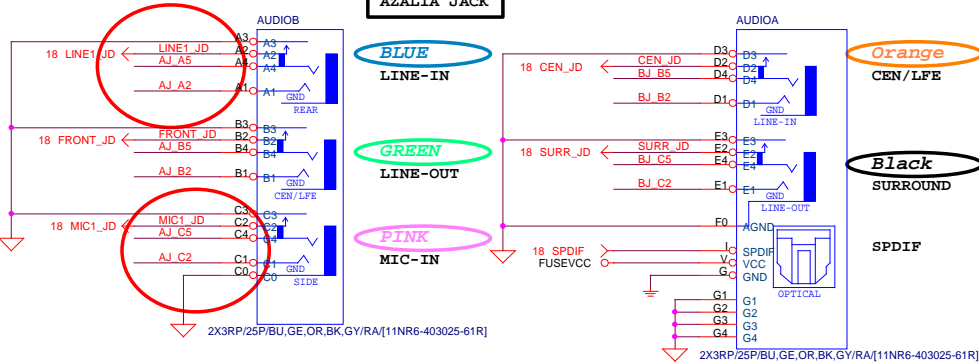
SPDIF_OUT



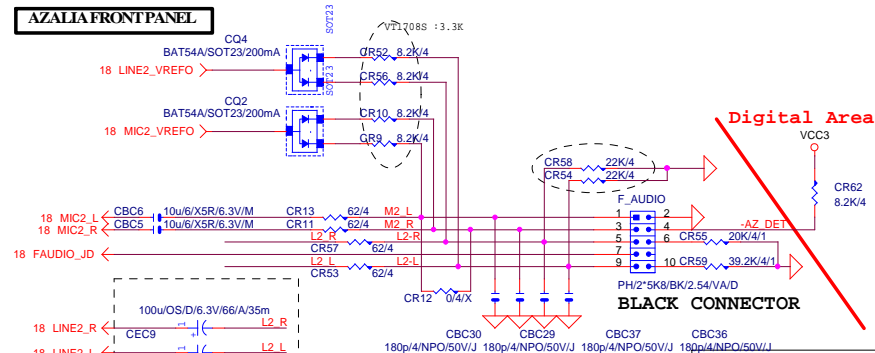
AZALIA JACK



AZALIA JACK

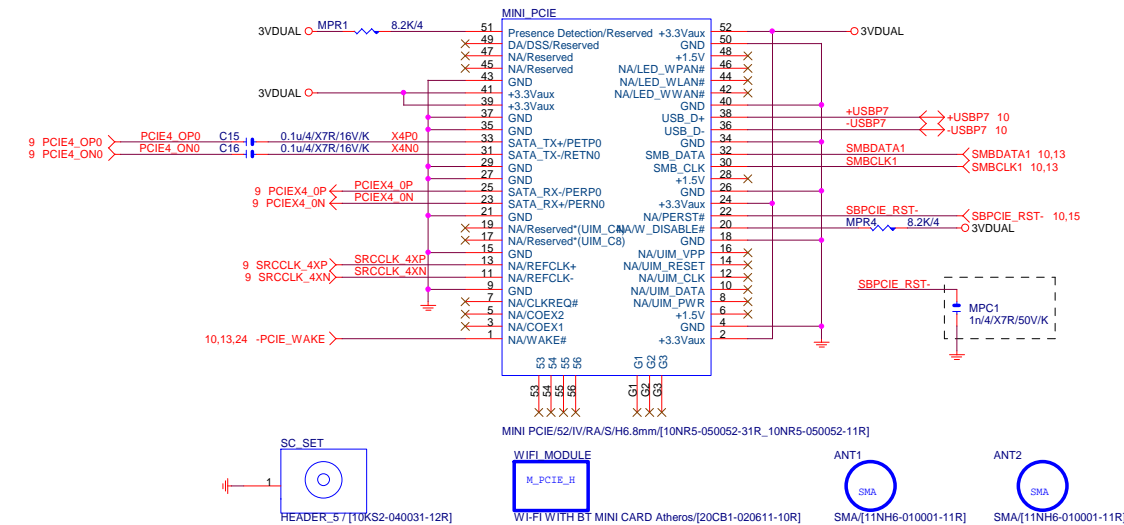
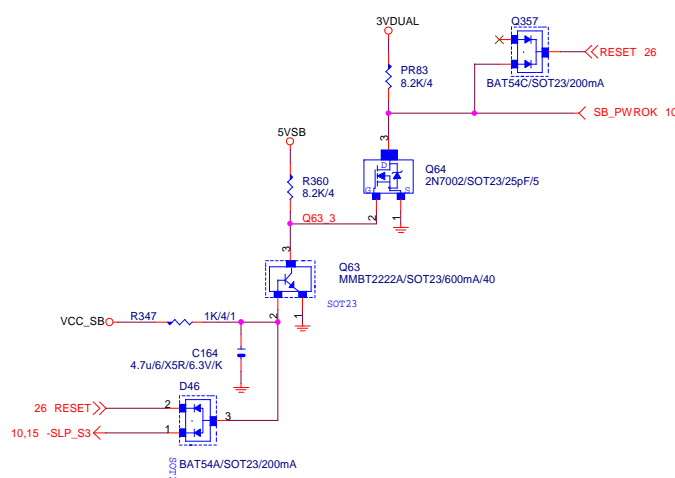
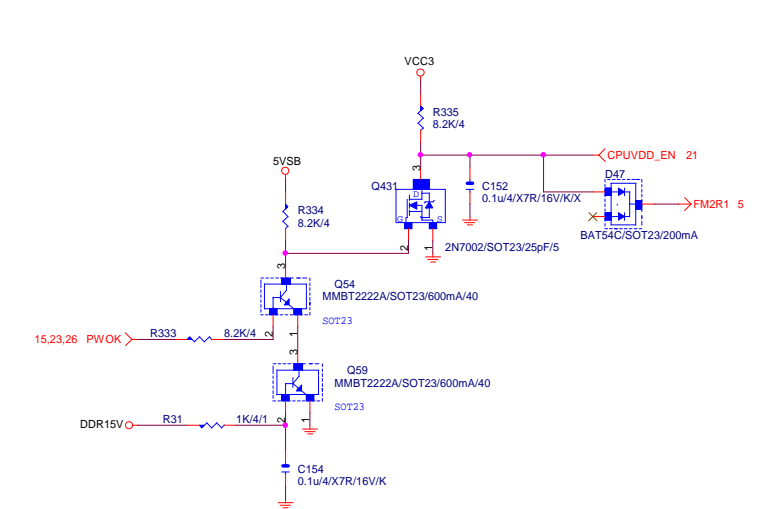
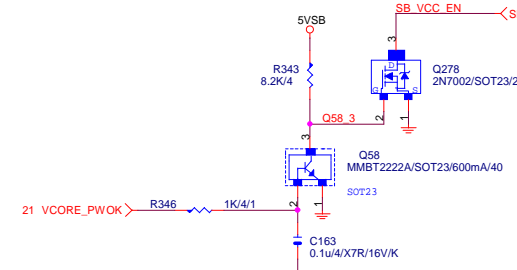
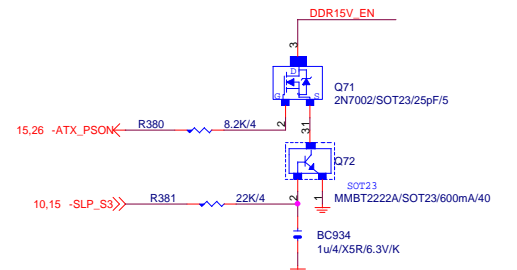
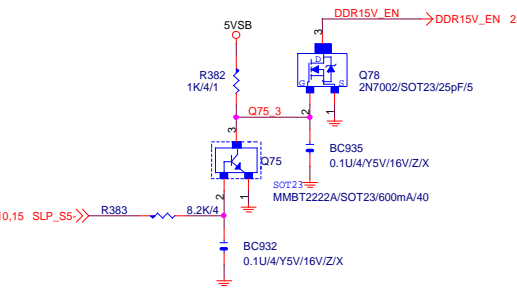


AZALIA FRONT PANEL



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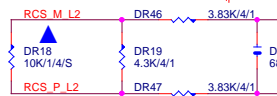
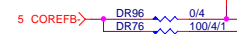
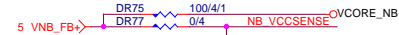
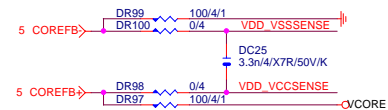
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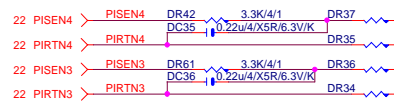
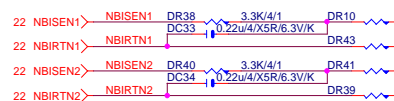
GIGABYTE™			
Title			
POWER SEQUENCE ,MINI_PCIE			
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20 CPUVDD_EN DR58 0/4/SHT/X PWM_EN



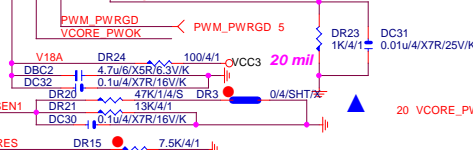
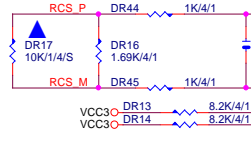
Differential 20/7/8/7/20

ISEN*VRTN* R and C closet to IC



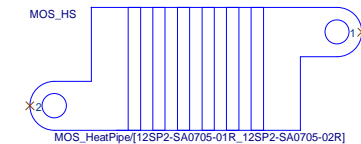
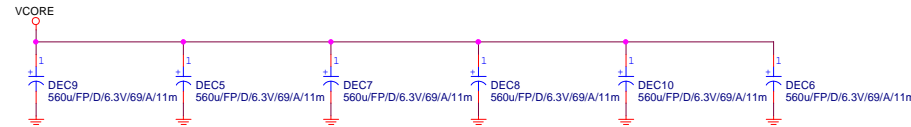
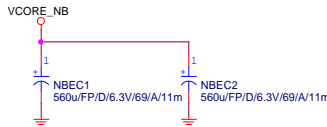
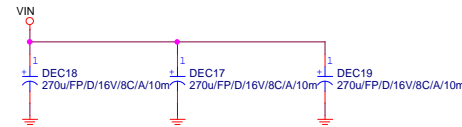
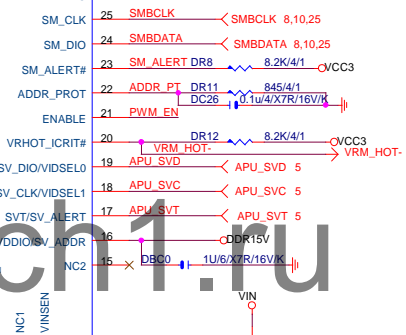
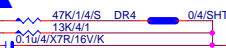
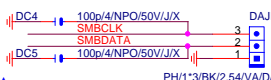
IR3567

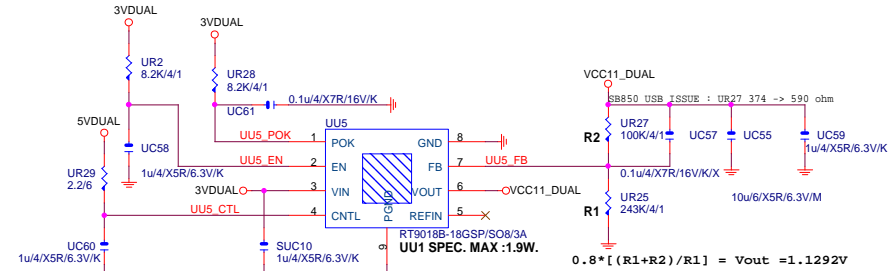
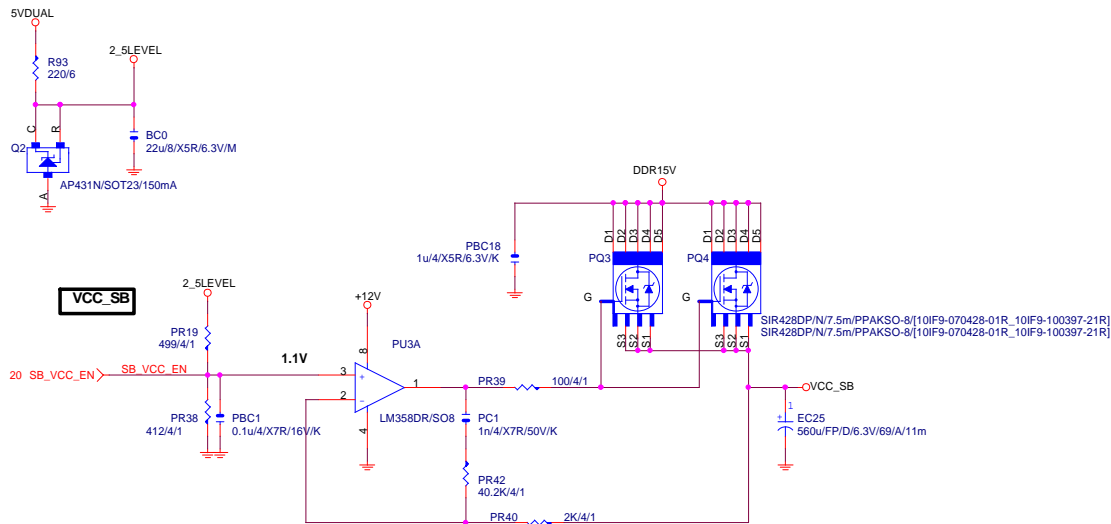
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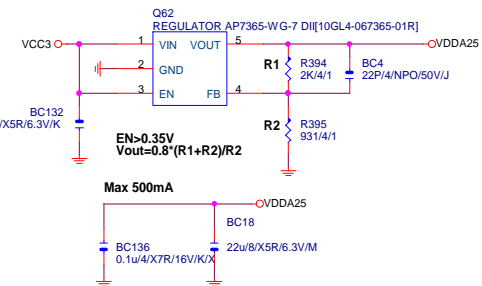
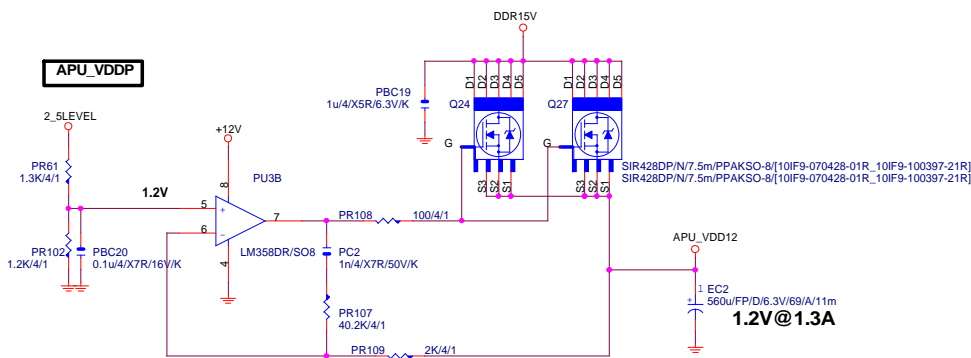
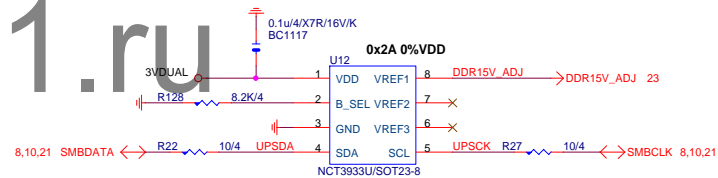
DR18 close to DG_DL1,
DR17 close to DA_DL1,
DR1 close to DG_DU1,
DR20 close to DB_DU1

DBC12, DR15, DBC2,
DC32 close to IC asap.





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Title			
VCC_SB , APU VDDP,VCC11_DUAL,VDDA25			
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